



P.B.5818 - Patentlaan 2  
2280 HV Rijswijk (ZH)  
+31 70 340 2040  
TX 31651 epo nl  
FAX +31 70 340 3016

Eur päisches  
Patentamt

Zweigstelle  
in Den Haag  
Recherchen-  
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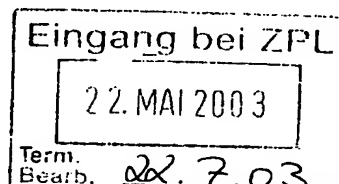
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recherche

Brose, Gerhard, Dipl.-Ing.  
Alcatel  
Intellectual Property Department, Stuttgart  
70430 Stuttgart  
ALLEMAGNE



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ALCATEL

## COMMUNICATION

The European Patent Office herewith transmits as an enclosure the European search report for the above-mentioned European patent application.

If applicable, copies of the documents cited in the European search report are attached.

☐ Additional set(s) of copies of the documents cited in the European search report is (are) enclosed as well.

The following specifications given by the applicant have been approved by the Search Division:

☐ abstract

☐ title

☒ The abstract was modified by the Search Division and the definitive text is attached to this communication.

The following figure will be published together with the abstract: 4

## REFUND OF THE SEARCH FEE

If applicable under Article 10 Rules relating to fees, a separate communication from the Receiving Section on the refund of the search fee will be sent later.





DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
D, A <sup>A</sup> <i>cited</i>	US 6 278 718 B1 (ESCHHOLZ SIEGMAR K) 21 August 2001 (2001-08-21) * abstract * * column 3, line 13 - column 4, line 62 * * column 7, line 39 - column 8, line 59 * * column 9, line 8 - column 13, line 9 * * figures 3,6-8 *	1-4 ✓	H04J3/06 H04L12/00
D, A <sup>A</sup> <i>cited</i>	US 6 166 963 A (WEN SHEUNG-FAN) 26 December 2000 (2000-12-26) * abstract * * column 1, line 14 - column 2, line 23 * * column 3, line 1 - column 4, line 48 *	1-4 ✓	
A <sup>A</sup>	GB 2 362 777 A (3COM CORP) 28 November 2001 (2001-11-28) * abstract * * page 1, line 6 - page 2, line 7 * * page 3, line 7 - line 12 * * page 4, line 15 - line 26 * * page 5, line 22 - page 7, line 4 * * figure 1 *	1-4 ✓	
A	GB 2 275 852 A (SONY UK LTD ; SONY BROADCAST & COMMUNICATION (GB)) 7 September 1994 (1994-09-07) * abstract * * page 1, line 19 - line 35 * * page 3, line 9 - page 4, line 26 * * page 6, line 9 - line 24 * * page 7, line 10 - page 8, line 1 *	1-4	H04J H04L
A	FR 2 693 333 A (THOMSON CSF) 7 January 1994 (1994-01-07) * abstract * * page 4, line 30 - page 8, line 25 * * figure 11 *	1-4	
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 8 May 2003	Examiner Carballo da Costa, E
<b>CATEGORY OF CITED DOCUMENTS</b> X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 02 36 0358

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
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08-05-2003

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## ABSTRACT / ZUSAMMENFASSUNG / ABREGE

02360358.2

Method and system for handling data between a clock and data recovery circuit and a data processing unit in asynchronous networks.

The invention relates to a method for handling data between a clock and data recovery system CDR and a data processing unit DP of a telecommunications network node TNN of an asynchronous communications network, using a bit rate adaptation circuit BAS, the bit rate adaptation system BAS comprising a memory unit MEM with a write process circuit Wp controlled by the recovered clock Rclk and a read process circuit Rp controlled by the local clock Lclk wherein the bit rate adaptation system BAS also comprises a pointer synchronization controller PSC which, depending on the data detected on the input data signal Dlb1 of the bit rate adaptation system BAS, sets the read and write pointers to a fixed initial address value.

The invention also relates to a bit rate adaptation circuit BAS, which implements the method described above and a Clock and Data Recovery system and a telecommunications network node TNN of an asynchronous network, which comprise a bit adaptation circuit BAS according to the invention.